IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hui Zhang et al. Serial No.: 10/767.001

Practitioner's Docket No : 1856 Examiner: Lee, Christopher E.

Filing Date: 01/29/2004 Art Unit: 2111

For: Buffer Bypass Circuit for Reducing Latency in Information Transfers to a Bus

Confirmation No. 5019

Mail Stop Amendment Commissioner for Patent P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

In response to the Non-Final Examiner's Action mailed on November 2, 2006, the aboveidentified patent application is amended as follows. Applicants have elected to present the amendment using the revised amendment format set forth in the waiver of 37 CFR 1.121.

AMENDMENT B

(37 C.F.R. § 1.111)

Amendments to the claims begin of page 2 of this paper.

Remarks begin on page 6 of this paper.

Serial No : 10/767 001 1 IDT-1856